#### **CMLT7820G**

# SURFACE MOUNT SILICON VERY LOW V<sub>CE(SAT)</sub> PNP TRANSISTOR



#### **APPLICATIONS:**

- DC/DC Converters
- Voltage Clamping
- Protection Circuits
- Battery powered Cell Phones, Pagers, Digital Cameras, PDAs, Laptops, etc.

SOT-563 CASE

# Central Semiconductor Corp.

www.centralsemi.com

## **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMLT7820G is a very low  $V_{\text{CE}(\text{SAT})}$  PNP Transistor, designed for applications where small size and efficiency are the prime requirements. Packaged in a space saving SOT-563 surface mount package, this component provides performance characteristics suitable for the most demanding size constrained applications.

**MARKING CODE: 78G** 

## **FEATURES:**

- Device is Halogen Free by design
- High Current (IC=1.0A)
- VCE(SAT)=0.34V MAX @ IC=1.0A
- SOT563 surface mount package
- Complementary NPN device CMLT3820G

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V <sub>CBO</sub>	80	V
Collector-Emitter Voltage	VCEO	60	V
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	V
Continuous Collector Current	I <sub>C</sub>	1.0	Α
Peak Collector Current	ICM	2.0	Α
Continuous Base Current	lΒ	300	mA
Power Dissipation	$P_{D}$	250	mW
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
Thermal Resistance	ΘιΔ	500	°C/W

## **ELECTRICAL CHARACTERISTICS:** (T<sub>A</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =60V		100	nA
I <sub>EBO</sub>	V <sub>EB</sub> =5.0V		100	nA
BV <sub>CBO</sub>	I <sub>C</sub> =100μA	80		V
<b>BV</b> CEO	I <sub>C</sub> =10mA	60		V
BVEBO	I <sub>E</sub> =100μA	5.0		V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =100mA, I <sub>B</sub> =1.0mA		0.175	V
VCE(SAT)	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		0.18	V
VCE(SAT)	I <sub>C</sub> =1.0A, I <sub>B</sub> =100mA		0.34	V
V <sub>BE</sub> (SAT)	I <sub>C</sub> =1.0A, I <sub>B</sub> =50mA		1.1	V
V <sub>BE</sub> (ON)	$V_{CE}$ =5.0V, $I_{C}$ =1.0A		0.9	V
h <sub>FE</sub> ` ´	$V_{CE}$ =5.0V, $I_{C}$ =1.0mA	200		
hFE	$V_{CE}$ =5.0V, $I_{C}$ =500mA	150		
h <sub>FE</sub>	$V_{CE}$ =5.0V, $I_{C}$ =1.0A	100		
$f_{T}$	$V_{CE}$ =10V, $I_{C}$ =50mA	150		MHz
C <sub>ob</sub>	$V_{CB}$ =10V, $I_E$ =0, f=1.0MHz		15	pF

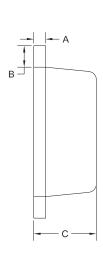
R4 (29-June 2015)

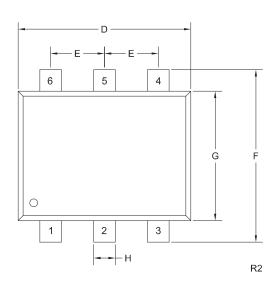
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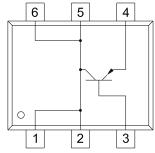


# **SOT-563 CASE - MECHANICAL OUTLINE**





## **PIN CONFIGURATION**



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.0027	0.007	0.07	0.18			
В	0.008		0.20				
С	0.017	0.024	0.45	0.60			
D	0.059	0.067	1.50	1.70			
E	0.020		0.50				
F	0.059	0.067	1.50	1.70			
G	0.043	0.051	1.10	1.30			
Н	0.006	0.012	0.15	0.30			
SOT-563 (REV: R2)							

## **LEAD CODE:**

- 1) Collector
- 2) Collector
- 3) Base
- 4) Emitter
- 5) Collector
- 6) Collector

Pins 1, 2, 5 and 6 are common.

# MARKING CODE:78G

R4 (29-June 2015)

## **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

#### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

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