

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
-40V	13mΩ @ V <sub>GS</sub> = -10V	-10.3A
-40 V	18mΩ @ V <sub>GS</sub> = -4.5V	-8.8A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

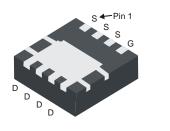
- Backlighting
- Power Management Functions
- DC-DC Converters

### **Features and Benefits**

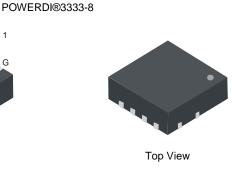
- Low R<sub>DS(ON)</sub> ensures on state losses are minimized.
- Small form factor thermally efficient package enables higher density end products.
- Occupies 33% of the board area occupied by SO-8, enabling smaller end product.
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

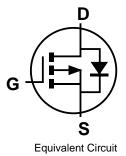
### **Mechanical Data**

- Case: POWERDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.072 grams (Approximate)



Bottom View





### Ordering Information (Note 4)

Part Number	Case	Packaging
DMP4013LFG-7	POWERDI®3333-8	2,000/Tape & Reel
DMP4013LFG-13	POWERDI®3333-8	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

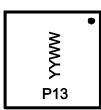
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**

#### POWERDI®3333-8



P13= Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 13 = 2013) WW = Week Code (01 ~ 53)

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# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	-40	V		
Gate-Source Voltage	V <sub>GSS</sub>	±20	V		
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-10.3 -8.3	А
	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-13.7 -11	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	IDM	80	А		
Maximum Continuous Body Diode Forward Current	Is	2.6	А		
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	34	A		
Avalanche Energy, L = 0.1mH			EAS	58	mJ

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)		PD	1	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	123	°C/W	
Thermal Resistance, sunction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	69	0/11	
Total Power Dissipation (Note 6)		PD	2.1	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	60	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ heta JA}$	34		
Thermal Resistance, Junction to Case (Note 6)		$R_{ ext{ heta}JC}$	3.3		
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	1 -						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-40	_	—	V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	—	-1	μA	$V_{DS} = -40V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)				•	•	•	
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1	—	-3	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance	Р		9.4	13	mΩ	$V_{GS} = -10V, I_D = -10A$	
	R <sub>DS (ON)</sub>		12.3	18	11152	$V_{GS} = -4.5V, I_D = -8A$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)						-	
Input Capacitance	C <sub>iss</sub>	_	3,426	—	pF	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss	_	283	—	pF		
Reverse Transfer Capacitance	Crss		235	—	pF		
Gate Resistance	Rg		4.7	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg		32.5	—	nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg		68.6	—	nC		
Gate-Source Charge	Q <sub>gs</sub>		8.2	—	nC	$V_{DS} = -20V, I_D = -10A$	
Gate-Drain Charge	Q <sub>gd</sub>		9.9	—	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	5.3	—	ns		
Turn-On Rise Time	tr	_	20	_	ns	$V_{DD} = -20V, V_{GEN} = -10V,$ $R_G = 3\Omega, I_D = -10A$	
Turn-Off Delay Time	t <sub>D(off)</sub>		126	_	ns		
Turn-Off Fall Time	t <sub>f</sub>	_	83	_	ns	1	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	19.5	—	nS		
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		9.8	—	nC	I <sub>F</sub> = -10A, di/dt = 100A/μs	

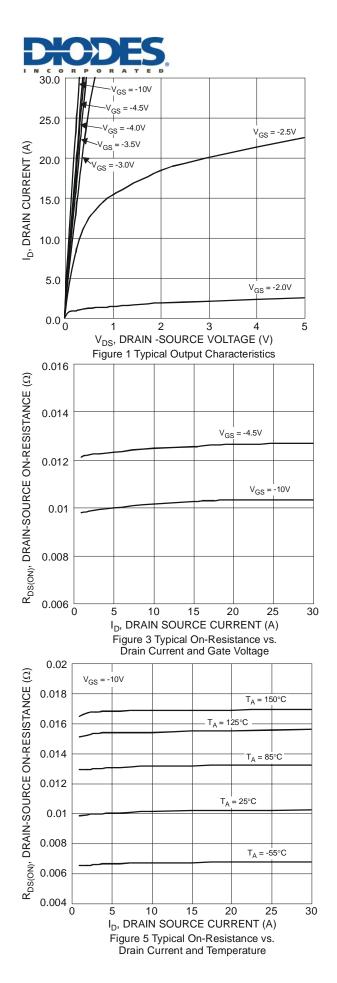
Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

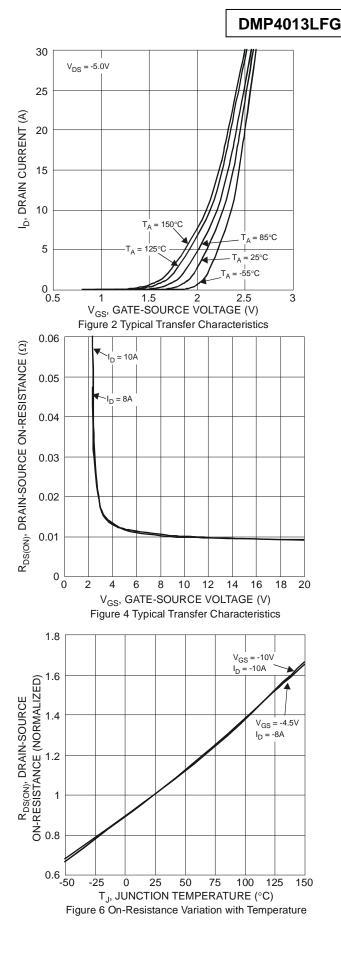
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

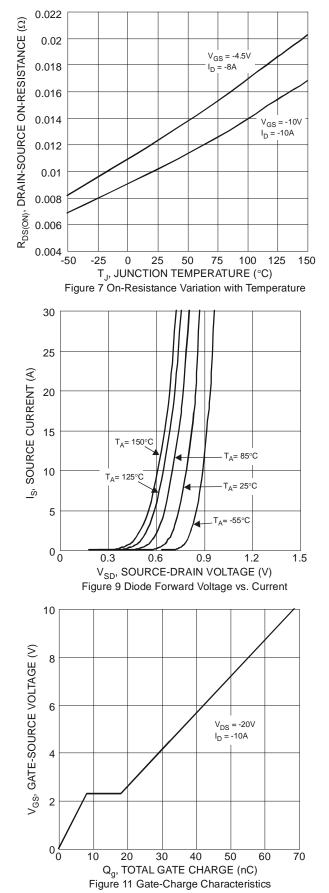
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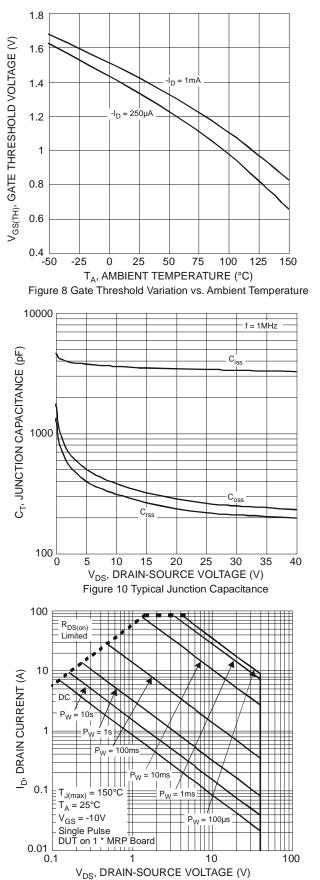
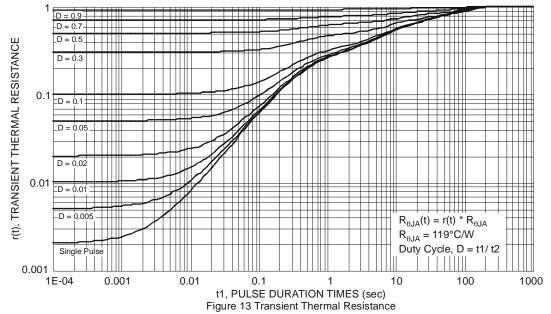


Figure 12 SOA, Safe Operation Area

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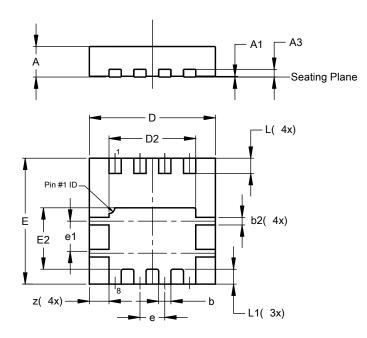






## **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

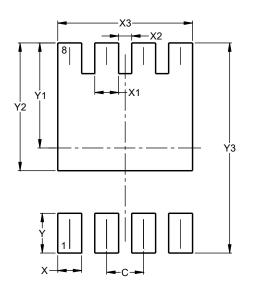


POWERDI <sup>®</sup> 3333-8						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	-	-	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
Е	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
е	-	-	0.65			
e1	0.79	0.89	0.84			
L	0.35	0.45	0.40			
L1	_	_	0.39			
z	_	-	0.515			
All I	All Dimensions in mm					

#### POWERDI®3333-8

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



#### POWERDI®3333-8

Dimensions	Value (in mm)		
С	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
X3	2.370		
Y	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		



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