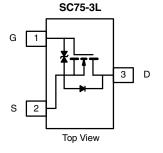


**Vishay Siliconix** 

## N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A) <sup>a</sup> Q <sub>g</sub> (Typ.		
	0.420 at V <sub>GS</sub> = 4.5 V	0.606		
20	0.501 at V <sub>GS</sub> = 2.5 V	0.505	0.92	
	0.660 at V <sub>GS</sub> = 1.8 V	0.150		



Si1046R-T1-GE3 (Lead (Pb)-free and Halogen-free)

**Ordering Information:** 

FEATURES

- TrenchFET<sup>®</sup> Power MOSFET: 1.8 V Rated
- ESD Protected: 2000 V
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912

#### **APPLICATIONS**

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- **Battery Operated Systems** ٠
- **Power Supply Converter Circuits**
- Load/Power Switching Cell Phones, Pagers

Marking Code: J

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ , unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20	V	
Gate-Source Voltage		V <sub>GS</sub>	± 8	1	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	0.606 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		0.485 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	2.5		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	۱ <sub>S</sub>	0.21 <sup>b, c</sup>		
Maximum Davier Dissingtional	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.25 <sup>b, c</sup>	w	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	U I	0.16 <sup>b, c</sup>	~~~~	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manimum hunsting to Angleingth d	t ≤ 5 s	R <sub>thJA</sub>	440	530	°C/W	
Maximum Junction-to-Ambient <sup>b, d</sup>	Steady State		540	650	0/10	

Notes:

a. Based on  $T_C = 25$  °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 650 °C/W.



RoHS

COMPLIANT HALOGEN

FREE

## Si1046R

## Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static		l		•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	20			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 050 4		20.5		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	- I <sub>D</sub> = 250 μA		- 2.12			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	0.35		0.95	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 30	mA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
		$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 85 ^{\circ}\text{C}$			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}$ = $\geq$ 5 V, $V_{GS}$ = 4.5 V	2.5			А	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 0.606 \text{ A}$		0.336	0.420	1	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 0.505 A		0.395	0.501	Ω	
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 0.150 A		0.438	0.660		
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.606 A		2.1		S	
Dynamic <sup>b</sup>	•			I	<u> </u>		
Input Capacitance	C <sub>iss</sub>			66		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		17			
Reverse Transfer Capacitance	C <sub>rss</sub>			7			
		$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 5 \text{ V}, \text{ I}_{D} = 0.606 \text{ A}$	0.99		1.49		
Total Gate Charge	Qg			0.92	1.38	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 0.606 \text{ A}$		0.15			
Gate-Drain Charge	Q <sub>gd</sub>			0.30			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		212		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			17	26		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 20.8 $\Omega$		19	28.5	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 0.48$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$		76	114		
Fall Time	t <sub>f</sub>			27	41		
Drain-Source Body Diode Characteristic	cs	•					
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				2.5	Α	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 0.48 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			16	24	nC	
ody Diode Reverse Recovery Charge Q <sub>rr</sub>				4.8	7.2		
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = 1 A, dl/dt = 100 A/μs		12.3		ns	
Reverse Recovery Rise Time	t <sub>b</sub>	1		3.7			

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

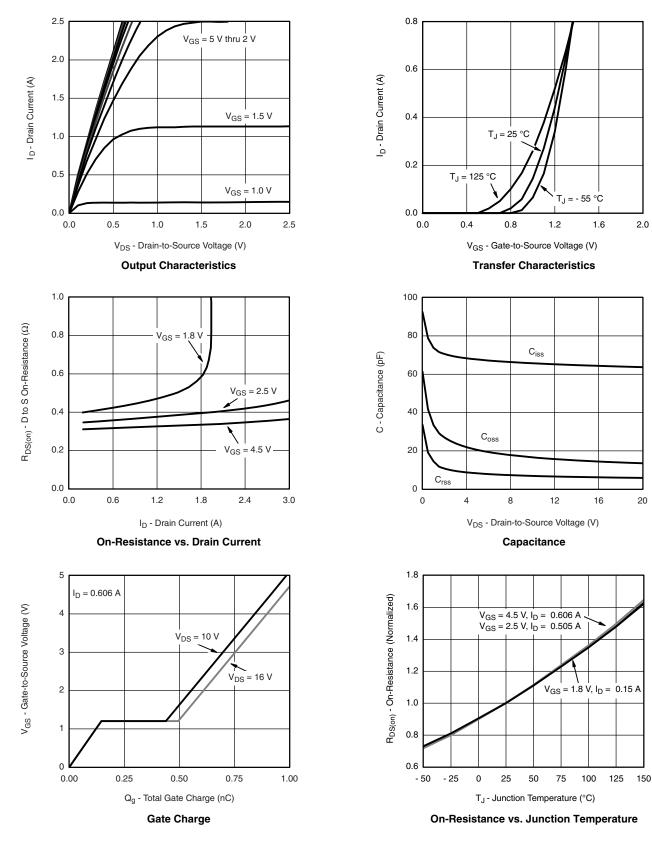
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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#### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



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For technical questions, contact: pmostechsupport@vishay.com

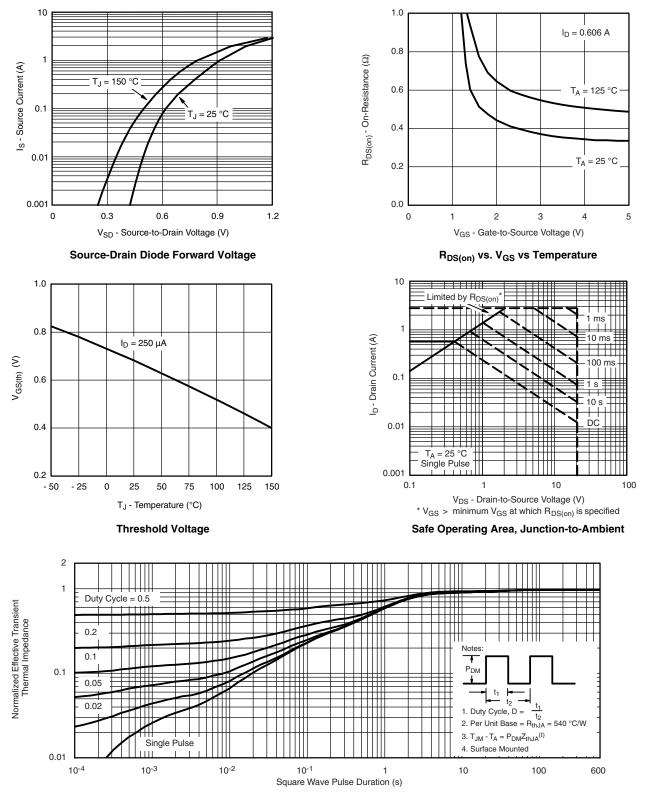
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## Si1046R

#### Vishay Siliconix

# VISHAY

#### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

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