



450V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C	
-450V	4.9Ω @ V _{GS} = -10V	-4.6A	

Description

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

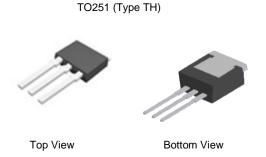
- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

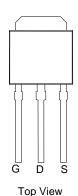
Features

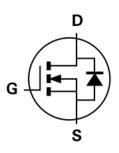
- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: TO251 (Type TH)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ³
- Weight: 0.33 grams (Approximate)







Internal Schematic

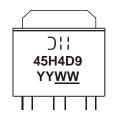
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP45H4D9HJ3	TO251 (Type TH)	75 pieces / tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	-450	V		
Gate-Source Voltage			V _{GSS}	±30	V
Continuous Drain Current (Note 5) V _{GS} = -10V	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	-4.6 -3.0	А
Maximum Body Diode Forward Current (Note 5)	Is	-3.0	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-22.4	Α
Avalanche Current, L = 60mH (Note 7)			I _{AS}	2.5	Α
Avalanche Energy, L = 60mH (Note 7)			Eas	187	mJ
Peak Diode Recovery dv/dt (V _{DD} = -400V, I _D = -2.0A)			dv/dt	50	V/ns

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_C = +25$ °C	0	104	W	
Total Power Dissipation (Note 5)	T _C = +100°C	P_{D}	41	VV	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ hetaJA}$	40	°C/W		
Thermal Resistance, Junction to Case (Note 5)	$R_{\theta JC}$	1.2	C/VV		
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

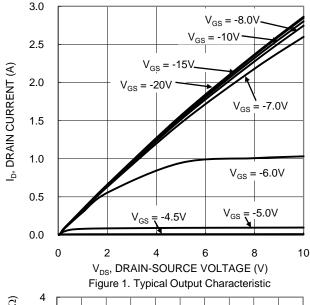
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-450		_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μA	$V_{DS} = -450V, V_{GS} = 0V$	
Gate-Source Leakage	I_{GSS}	_	_	±100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(TH)}$	-3.0	-4.0	-5.0	٧	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		3.1	4.9	Ω	$V_{GS} = -10V, I_D = -1.05A$	
Diode Forward Voltage	V_{SD}	_	_	-1.3	V	$V_{GS} = 0V, I_{S} = -2.1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		547	_		$V_{DS} = -25V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Output Capacitance	Coss		74	_	pF		
Reverse Transfer Capacitance	C_{rss}		3.1	_			
Total Gate Charge (V _{GS} = -10V)	Q_g		13.7	_			
Gate-Source Charge	Q_{gs}		3.4	_	nC	$V_{DS} = -360V$, $I_{D} = -2.7A$, $V_{GS} = -10V$	
Gate-Drain Charge	Q_{gd}		6.0	_			
Turn-On Delay Time	t _{D(ON)}		19	_		V_{DD} = -225V, R_G = 3.0 Ω , I_D = -2.7A	
Turn-On Rise Time	t _R	_	40	_	no		
Turn-Off Delay Time	t _{D(OFF)}	_	32	_	ns		
Turn-Off Fall Time	t _F		31	_			
Body Diode Reverse Recovery Time	t _{RR}	_	164	_	ns	$V_{GS} = 0V$, $V_{DD} = -200V$, $I_{S} = -2.7A$,	
Body Diode Reverse Recovery Charge	Q _{RR}		1.3		nC	dl/dt = 100A/µs	

Notes:

- 5. Device mounted on infinite heatsink.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 7. Guaranteed by design. Not subject to production testing.
 8. Short duration pulse test used to minimize self-heating effect.







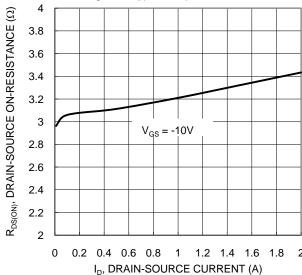


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

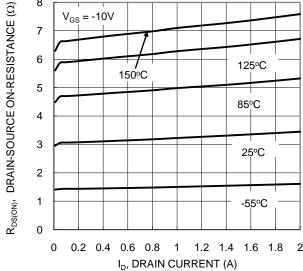
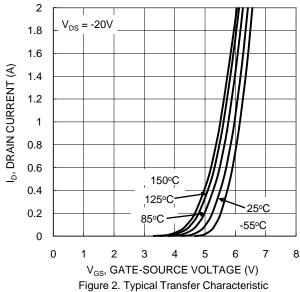
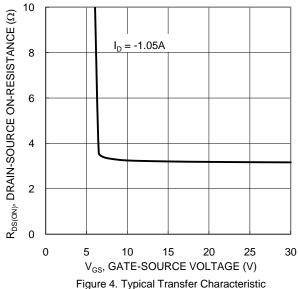


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





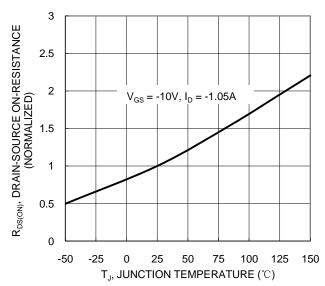


Figure 6. On-Resistance Variation with Temperature





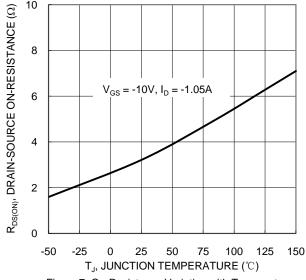
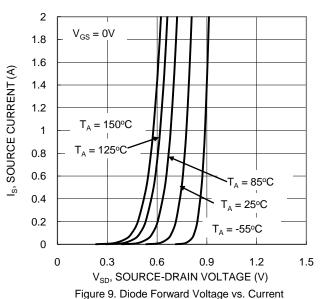


Figure 7. On-Resistance Variation with Temperature



10 8 6 $V_{DS} = -360V, I_{D} = -2.7A$ $V_{GS}(V)$ 4 2 0 0 2 6 8 10 12 14 Q_q (nC) Figure 11. Gate Charge

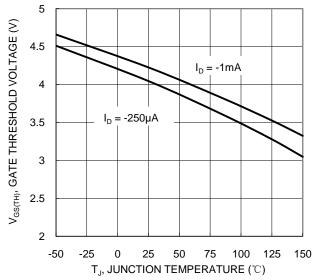
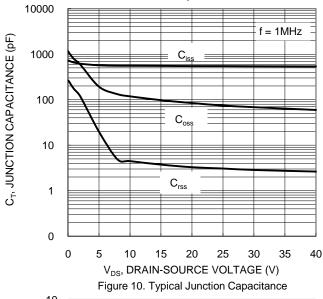


Figure 8. Gate Threshold Variation vs. Junction Temperature



10 $\begin{array}{c} R_{\text{DS(ON)}} \\ \text{Limited} \end{array}$ $P_W = 10\mu s$ ID, DRAIN CURRENT (A) = 100ms $T_{J(Max)} = 150$ °C $T_C = 25$ °C Single Pulse 10ms **DUT** on Infinite Heatsink $= 100 \mu s$ $V_{GS} = -10V$ 0.1 10 100 1000 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area



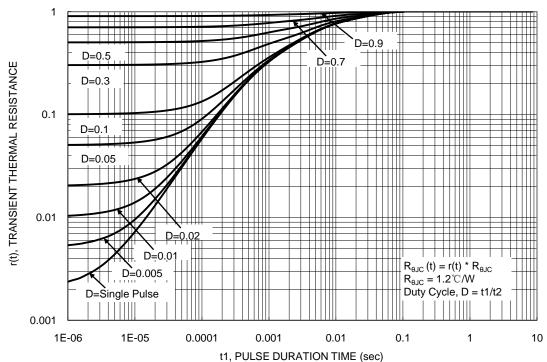


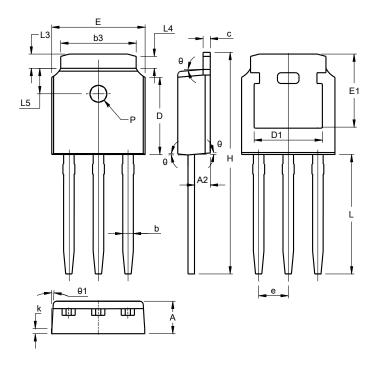
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO251 (Type TH)



TO251 (Type TH)						
Dim	Min	Тур				
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b3	5.20	5.50	5.33			
С	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	5	.30 RE	F			
е	2.	2.286 BSC				
Е	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
Н	16.22	16.82	16.52			
k	C).40REI	-			
L	9.15	9.65	9.40			
L3	0.88	1.28	1.02			
L4	0.75 REF					
L5	1.65	1.95	1.80			
PØ	1.20					
θ	5° 9°		7°			
θ1	5°	9°	7°			
All Dimensions in mm						



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